

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**RECEIVED
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MAR 10 2004

OFFICIAL

Applicants: DEBASHIS BHATTACHARYA et al.
Serial No: 09/896,059
For: METHOD FOR AUTOMATED DESIGN OF INTEGRATED
CIRCUITS WITH TARGETED QUALITY OBJECTIVES
USING DYNAMICALLY GENERATED BUILDING
BLOCKS

Filed: JUNE 29, 2001
Examiner: THOMPSON, ANNETTE M.
Art Unit: 2825
Confirmation No.: 9475
Customer No.: 27,623
Docket No.: 162.7107USU

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 2313-1450

AMENDMENT

*PLEASE ENTER,
21-SEP-2004*
Dear Sir:

In response to the Office Action dated September 25, 2003, for which the period to respond has been extended one month to include January 26, 2004, please enter and consider the following amendments and remarks in the above-noted application.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 2 of this paper.